AND THE COMPUTER

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#### WHY BIOLOGY CAN AND SILICON ... CAN'T

#### VALERIU BEIU

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The Neumann Computer and the Brain

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- ... arrays (of ion channels) Intermediate ... columnar structures
  - Higher levels ... sparsely connected (hierarchical)

... device(s) ... wire(s) ... system

### **Conclusions**

- **Device-level redundancy**
- Non-linear (active) "wires" (solitonic-like waves)
- **Communication/computation separated form power consumption** \*

IT IS CLEAR TO ME THAT WE WILL DEVELOP SILICON NEURAL SYSTEMS AND THAT LEARNING HOW TO DESIGN THEM



#### ONE OF THE GREATEST INTELLECTUAL QUESTS OF ALL TIMES

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**GARVER MEAD** 

Sven Geter 10





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### ... it's a grand challenge



Make solar energy economical

Provide energy from fusion

Develop carbon sequestration methods

Provide access to clean water

Restore and improve urban infrastructure

How close can machines come to human

Advance personalized learning

Engineer the tools of scientific discovery

#### Reverse-engineer the brain

The intersection of engineering and neuroscience promises great advances in health care, manufacturing, and communication.

> For decades, some of engineering's best minds have focused their thinking skills on how to create thinking machines — computers capable of emulating human intelligence.

#### Why should you reverse-engineer the brain?

While some of thinking machines have mastered specific narrow skills - playing chess, for instance - general-purpose artificial intelligence (AI) has remained elusive.

Part of the problem, some experts now believe, is that artificial brains have been designed without much attention to real ones. Pioneers of artificial intelligence approached thinking the way that aeronautical engineers approached flying without much learning from birds. It has ward and the make that the seconds should have living been and a

#### IMAGE GALLERY

NEXT STEPS

IDEAS

Images related to reverseengineering the brain





#### COMMITTEE MEMBERS SPEAK

The brain is a complex network. Understanding it has many benefits.



There are many practical reasons to reverse engineer the brain.



Charles Vest describes what is possible if we reverse engineer the brain.

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COMMITTEE





### Let's take a walk through ...

... the "Machinery of Mind"

<u>http://smithlab.stanford.edu/Smithlab/AT\_Movies\_fil</u> <u>es/Machinery\_of\_Mind\_SD.mov</u>

### Measurements ....

and stimulations

Two images from the Blue Brain project. See <a href="http://www.youtube.com/watch?v=90cj4NX87Yk">http://www.youtube.com/watch?v=90cj4NX87Yk</a> (or <a href="http://www.videosift.com/video/Brain-Synapses-and-Neurotransmission-3D-Animation">http://www.youtube.com/watch?v=90cj4NX87Yk</a> (or <a href="http://www.youtube.com/watch?tel:http://wwww.youtube.com/watch?t









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"In an actual cell, the pyrophosphate concentration is kept low by hydrolysis, ensuring that only the copying process occurs, not its inverse. The whole RNA polymerase system is not particularly efficient as far as energy use goes: ... it dissipates about 100 kT per bit. Less could be wasted if the enzyme moved a little more slowly (and of course, the reaction rate does vary with concentration gradient), but there has to be a certain speed for the sake of life!

Still, 100kT per bit is considerably more efficient than the 10<sup>8</sup> kT thrown away by a typical transistor!"

(37,600 kT in 90nm ... 15,300 kT in 65nm ...)









- CMOS inverter moves charges from V<sub>DD</sub> to C<sub>load</sub> (pMOS)
   ... and then from C<sub>load</sub> to GND (nMOS), so charges are lost
- Ion channels move charges from outside to inside (Na<sup>+</sup>)
   ... and then back from inside to outside (K<sup>+</sup>)
   Equivalently ... from V<sub>DD</sub> to C<sub>load</sub> ... and back to V<sub>DD</sub> (!)

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### Here is the brain's ultra low-power solution:

- Two different information carriers (but same type of charge)
- Two different switches (one for each carrier) + asynchronous
- Carriers move (through these switches) in opposite directions
- A third device consumes (very slowly / quasi-adiabatically)

V. Beiu, *MEES'10* <u>http://www.src.org/calendar/e003960/SessionV-Beiu.pdf</u> Tutorial at *WCCI'10* <u>http://education.ieee-cis.org/lectures/Conference-Tutorials/On-Brain-Inspired-Nano-Interconnects</u>





#### Moving a word across die = 124 MACs or 10 FMAs Moving a word off chip = 250 MACs or 20 FMAs

W.J. Dally at DAC'09 http://videos.dac.com/46th/wedkey/dally.html

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# There are two reasons why WSI is very difficult First, a typical digital chip will fail if even a single transistor or wire is defective

 Second, the power dissipated by several hundred chips is over 100W and getting rid of all that heat is a major problem



These two problems have prevented even the largest companies from deploying WSI systems successfully. Carver Mead

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![](_page_21_Picture_1.jpeg)

![](_page_21_Picture_2.jpeg)

**Cable theory** 1930 **Hodgkin-Huxley** 1952 **Fitzhug-Nagumo**  $\diamond$ **Morris-Lecar Turner and Sens** Heimburg

**1961 traveling wave 1981 traveling wave 2004** (mechanical) 2005 solitons Quite a few "simplified" models (latest trend) None of these can be used for estimating either power or reliability

## The brain ... one level at a sur

Arrays

Almost fully

connected

Sub-cellular

Cell/neuron <

Columnar

BBP http://bluebrain.epfl.ch/

Region/area

???

Brain/whiteSparsely connectedHCPhttp://www.humanconnectomep

Boolean

Inner product Threshold, WTA

Interesting functions ...

![](_page_23_Figure_0.jpeg)

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## Axon-like (simplistic) communication

![](_page_24_Picture_1.jpeg)

V. Beiu et al., IEEE-NANO'11, ECCTD'11, and forthcoming special issue of Frontiers in Computational Neuroscience

Latest results

#### 2D arrays can be extremely reliable

 At least 100 better than von Neumann multiplexing

![](_page_25_Figure_4.jpeg)

- 10% failing nodes + 10% failing inputs + 30% placement (x,y) + failing links ...
- Could achieve 0.01nJ/cm
  3D even better
- 10<sup>0</sup> **10**<sup>-2</sup> 10-4 10-6 10-8 PFARRAY Jare at 30° **10**<sup>-10</sup> Square at 0% 10-12 **10**<sup>-14</sup> 10 Eailure 10-16 ď 10<sup>-€</sup> **10**<sup>-18</sup> Triangle at 0% 2 Normalized Wire Length 10-20 **10**<sup>-10</sup> 10-8 10-6 10-4 **10**<sup>-2</sup> **10<sup>0</sup>** PF<sub>NODE</sub> 26

![](_page_26_Figure_0.jpeg)

## Communication + processing

V. Beiu, GCoE'08

![](_page_28_Picture_0.jpeg)

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![](_page_29_Picture_1.jpeg)

#### Europe

- FACETS and BrainScaleS
- ✤ BlueBrain
- ✤ Human Brain Project (HBP)

## n<mark> Brain</mark> Pi

#### EU ~ 30 M\$ Switzerland EU ~ 1 BEuro

### USA

- SyNAPSE DARPA
   Human Connectome NIH
   Brain Research through Advancing Innovative NSF Nanotechnologies = BRAIN DARPA (functional connectomics)
- ~ 50 M\$ ~ 40 M\$ > 300 M\$

Until now we have been going the other way; that is, in order Don't be encumbered by history. Go off and do something wonderful. to understand the brain we have to use the computer as a model of it.

Perhaps it is time to reverse this reasoning: to understand where we should go with the computer, we should look to the brain for some clues.

Robert Noyce, IEEE Centenary "The Next 100 Years" ... 1984

![](_page_31_Figure_0.jpeg)

K. Zhang & T.J. Sejnowski, PNAS, 2000

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Hierarchical networks

N<sub>CONN</sub>

 $N_{\text{CLUSTERS}} \times N_{\text{CONN}}(\text{per cluster}) + N_{\text{CONN}}(\text{amongst clusters}) \times [1 \dots N_{\text{PROC}}(\text{per cluster})]$ 

Local network
Global network
Hierarchical network
Solving for *b* gives

 $m^{1+a}$   $(N/m)^{1+b}$   $N_{CONN} = Nm^a + N(N/m)^b$  $\log(F_{IN} - m^a)/(\log N - \log m)$ 

For *fan-in*  $= m^a$  the global network becomes simple ... only a few (but wider) global interconnects

V. Beiu et al., NanoArch'08 and IC-SAMOS'08

![](_page_33_Picture_2.jpeg)

![](_page_33_Figure_3.jpeg)

![](_page_33_Picture_5.jpeg)

![](_page_33_Figure_6.jpeg)

### Columnar structures

... "How do neurons connect to each other?"

http://www.youtube.com/watch?v=ySgmZ0TkQA8& list=UUIMJeVIVyGp-3\_kWtspkS0Q

### Brain is (at least) 3-layered

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The first layer is represented by the neuron itself ... active membrane ... solitonic transmission (cellular-like ... but not systolic/CNN/QCA, etc.) This level is essential for reliability, while also taking care of for ultra-low power (requires both novel devices and "cellular-like" architectures)

The second layer corresponds to the columnar structures. Highly connected (depending on the neurons' fan-in).

## The third layer corresponds to the white matter ... sparsely connected using high(er)-speed "wires"

V. Beiu et al., DCIS'07, ISCAS'08, NanoArch'08, Nano-Net'09, and Trans. HiPEAC, 2009

Columns = Circuits with feedback

![](_page_36_Figure_1.jpeg)

$$f_{1} = x_{1}y$$

$$f_{2} = x_{2} + f_{1} = x_{2} + x_{1}y$$

$$f_{3} = x_{3}f_{2} = x_{3}(x_{2} + x_{1}y)$$

$$f_{4} = x_{1} + f_{3} = x_{1} + x_{3}(x_{2} + x_{1}y) = x_{1} + x_{2}x_{3}$$

$$f_{5} = x_{2}f_{4} = x_{2}(x_{1} + x_{2}x_{3}) = x_{2}(x_{1} + x_{3})$$

$$f_{6} = x_{3} + f_{5} = x_{3} + x_{2}(x_{1} + x_{3}) = x_{3} + x_{1}x_{2}.$$

The view that the brain is a very complex, highly interconnected system of unreliable analog processing units is not an incorrect one, while unfortunately it is ... an incomplete/partial one

- ✤ Behaves digitally
- The elementary switches behave (very much) like
- Behaves (very much) as self-timed
- Can be seen as analog
- **Has 10<sup>19</sup> (large redundancy)**
- Neurons are quite reliable

... at the lowest level

... single ion devices

... from the lowest level ... at the system level ... digital switches ... significant redundancy

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### ... and many misconceptions

**Brain is analog Brain is slow** Low power due to speed  $\diamond$ Low power due to low V Low power due to 3D Low power due to spikes **Neurons are not reliable** 

> Using learning/adaptation leads to reliable structures

... SET (but many) .... 10<sup>7-8</sup> ions/sec ... due to ion pumps ... **P** is not **IxV** (!) ... Reliability due to 3D ... On the contrary ... Yes, they are! [Synapses are not ...] ... Only if "devices" are reliable "enough"

![](_page_39_Picture_2.jpeg)

lons (several carriers) **Diffusion (creates V)** lon channel 10° SET 103-4 **SETs** Axon 106-7 **SETs** Neuron 1010-11 Column SETs Area **10**18-19 **Brain** SETs

Brain

### Plans for the future

HH-equivalent (model/equations) for power/energy
 Communication between ion channels (statistically large fan-ins)

Logical functioning of an ion channel (probabilistic TLGs)

#### HH-equivalent (model equations) for reliability

- Statistical analysis of arrays of ion channels
- CAD + Bayesian tools for arrays of ion channels

#### **Columnar structure**

- Theory of circuits with feedback (and large fan-ins)
- CAD tools for simulations/synthesis

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### Should lead to novel and detailed ...

**Understanding of the functioning of the Brain** (at the lower levels)

![](_page_41_Picture_3.jpeg)

#### CAD tools

![](_page_41_Picture_5.jpeg)

![](_page_42_Picture_0.jpeg)

![](_page_43_Picture_0.jpeg)

![](_page_43_Picture_1.jpeg)